| | Document ID | Issue Date | Pages | Title | Current OR |
|----|-------------------------|------------|-------|---|---------------|
| 1 | US 20050237981 A1 | 20051027 | 18 | Ultra wide band communication network | 370/337 |
| 2 | US 20030156603 A1 | 20030821 | 149 | Apparatus and method for trellis encoding data for transmission in digital data transmission systems | 370/485 |
| 3 | US 20030142764 A1 | 20030731 | | Single chip VLSI implementation of a digital receiver employing orthogonal frequency division multiplexing | 375/341 |
| 4 | US 20020015423 A1 | 20020207 | 148 | Apparatus and method for trellis encoding data for transmission in digital data transmission systems | 370/485 |
| 5 | US 20010046266 A1 | 20011129 | 149 | Apparatus and method for scdma digital data transmission using orthogonal codes and head end modem with no tracking loops | 375/259 |
| 6 | US 20010024474 A1 | 20010927 | 149 | Apparatus and method for trellis encoding data for transmission in digital data transmission systems | 375/259 |
| 7 | US 20010001616 A1 | 20010524 | 149 | Apparatus and method for SCDMA digital data transmission using orthogonal codes and a head end modem with no tracking loops | 375/259 |
| 8 | US 6961314 B1 | 20051101 | | Burst receiver for cable modem system | 370/252 |
| 9 | US 6937617 B2 | 20050830 | 129 | Apparatus and method for trellis encoding data for transmission in digital data transmission systems | 370/485 |
| 10 | US 6687315 B2 | 20040203 | 83 | Single chip VLSI implementation of a digital receiver employing orthogonal frequency division multiplexing | 375/341 |
| 11 | US 6665308 B1 | 20031216 | | Apparatus and method for equalization in distributed digital data transmission systems | 370/441 |
| 12 | US 6621794 B1 | 20030916 | 17 | Method and apparatus for measuring the timing difference between physical IMA links and for delivering a time difference to the IMA layer | 370/235 |

| | Current XRef | Inventor |
|----|---|-------------------------------|
| 1 | 370/347 | Aiello, Roberto et al. |
| 2 | | Rakib, Selim Shlomo et al. |
| 3 | 375/354; 714/795 | Keevill, Peter A et al. |
| 4 | 370/487; 370/503; 370/516 | Rakib, Selim Shlomo et al. |
| 5 | 375/354; 375/371 | Rakib, Selim Shlomo et al. |
| 6 | 375/354 | Rakib, Selim Shlomo et al. |
| 7 | 375/344 | Rakib, Selim Shlomo et al. |
| 8 | 370/352; 375/222 | Quigley; Thomas J. et al. |
| 9 | 370/479 | Rakib; Selim Shlomo et al. |
| 10 | 375/326 | Keevill; Peter A et al. |
| 11 | 370/442; 370/479; 370/503; 375/222; 375/233 | Rakib; Selim Shlomo et al. |
| 12 | 370/236.2 | Heikkinen; Pekka et al. |

| | Document ID | Issue Date | Pages | Title | Current OR |
|----|------------------|------------|-------|---|---------------|
| 13 | US 6333940 B1 | 20011225 | | Integrated digital loop carrier system with virtual tributary mapper circuit | 370/506 |
| 14 | US 6307868 B1 | 20011023 | | Apparatus and method for SCDMA digital data transmission using orthogonal codes and a head end modem with no tracking loops | 370/485 |
| 15 | US 5812497 A | 19980922 | 14 | Hybrid-synchronous type clock synchronizing apparatus of which dominant gain greater than sum of other gains network therewith, and clock synchronizing method thereof | 368/10 |
| 16 | US 5787133 A | 19980728 | 1/1 | Signal modulation employing a pseudo- random sequence of pilot symbols | 375/366 |
| 17 | US 5666378 A | 19970909 | | High performance modem using pilot symbols for equalization and frame synchronization | 375/222 |
| 18 | US 5615177 A | 19970325 | 14 | Hybrid-synchronous type clock synchronizing apparatus of which dominant gain greater than sum of other gains, network therewith, and clock synchronizing method thereof | 368/10 |
| 19 | US 5526383 A | 19960611 | | Network control system for controlling relative errors between network nodes | 375/356 |
| 20 | US 5524029 A | 19960604 | 70 | Network control system for controlling relative errors between network nodes | 375/356 |
| 21 | US 5515401 A | 19960507 | 71 | Network control system for controlling relative errors between network nodes | 375/344 |
| 22 | US 5166924 A | 19921124 | 1 | Echo cancellation in multi-frequency differentially encoded digital communications | 370/289 |
| 23 | US 4912706 A | 19900327 | | Frame synchronization in a network of time multiplexed optical space switches | 370/507 |

| | Current XRef | Inventor |
|----|---|--------------------------------|
| 13 | 370/466; 370/509 | Baydar; Ertugrul et al. |
| 14 | 370/516; 375/325; 375/326 | Rakib; Selim Shlomo et al. |
| 15 | 368/156; 368/46; 375/356 | Yahata; Haruki |
| 16 | 375/367; 714/775 | Marchetto; Robert F. et al. |
| 17 | 375/231; 375/235; 375/340 | Marchetto; Robert F. et al. |
| 18 | 368/156; 368/46; 375/356 | Yahata; Haruki |
| 19 | 375/358; 455/423; 455/502 | Takenaka; Tetsuyoshi et al. |
| 20 | 370/350; 375/358; 455/502 | Takenaka; Tetsuyoshi et al. |
| 21 | 375/356; 455/502; 455/71 | Takenaka; Tetsuyoshi et al. |
| 22 | 370/291; 379/406.1 3 | Moose; Paul H. |
| 23 | 370/517; 375/358; 398/52; 398/55 | Eisenberg; Martin et al. |